

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: **CHENG CHUNG LIN AND LAIN-JONG LI**

For: **METHOD TO IMPROVE STABILITY AND RELIABILITY OF CVD LOW K DIELECTRIC**

50640 U.S. PTO  
09/579542  
05/26/00

05/26/00  
JC831 U.S. PTO

losed are:

2 sheets of drawing(s) - formal.

An assignment of the invention to **Taiwan Semiconductor Manufacturing Company**

An associate power of attorney

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEES
BASIC FEE				\$ 690.
TOTAL CLAIMS	20 -20=	0	x 18 =	\$ 0.
INDEP CLAIMS	3 -3=	0	x 78 =	\$ 0.
MULTIPLE DEPENDENT CLAIM PRESENTED		+ 260 =		
		SUB TOTAL		\$ 690.
		ASSIGNMENT		\$40.
		TOTAL		\$ 730.

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Respectfully submitted,

STEPHEN B. ACKERMAN, REG. NO. 37,761